

**TRANSMITTAL
FORM**

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Application Number	10/015,276
Filing Date	December 11, 2001
First Named Inventor	Hosoya, Naoki
Group Art Unit	2812
Examiner Name	Unassigned
Attorney Docket Number	16869P-036200US

Total Number of Pages in This Submission

/ 0

ENCLOSURES (check all that apply)☐ Fee Transmittal Form☐ Fee Attached☒ Amendment / Response☐ After Final☐ Affidavits/declaration(s)☐ Extension of Time Request☐ Express Abandonment Request☐ Information Disclosure Statement☐ Certified Copy of Priority Document(s)☐ Response to Missing Parts/
Incomplete Application☐ Response to Missing
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(for an Application)☐ Drawing(s)☐ Licensing-related Papers☐ Petition Routing Slip (PTO/SB/69)
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Individual name

Townsend and Townsend and Crew LLP

George B. F. Yee

Reg. No. 37,478*

Signature

Date

March 20, 2002

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No.: 16869P-036200US
Client Ref. No.: 340101528US1

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In re application of:

Naoki Hosoya et al.

Application No.: 10/015,276

Filed: December 11, 2001

For: METHOD AND APPARATUS FOR
INSPECTING DEFECTS IN A
SEMICONDUCTOR WAFER

Examiner: Unassigned

Art Unit: 2812

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Prior to examination of the above-referenced application, please enter the following amendments and remarks.

IN THE CLAIMS:

Please cancel claims 23 and 24. The remaining claims are shown below.

1. A method for inspecting a semiconductor wafer comprising:
receiving defect information representative of plurality of defects on a
semiconductor wafer;